

**Amendments to the Claims:**

This listing of claims will replace all prior versions and listings of claims in the application:

**Listing of Claims:**

1. (Currently amended) A PLL circuit comprising:

an oscillation circuit that generates an oscillation signal with an oscillation frequency based on a supplied voltage;

a frequency divider that divides the frequency of the ~~generated~~ oscillation signal based on a predetermined frequency dividing number to generate a comparison signal;

a phase comparator configured to generate a phase difference signal indicative of a phase difference between the generated comparison signal and a reference signal;

a low-pass filter that generates a voltage signal formed as a direct current from the phase difference signal and that supplies the voltage signal to the oscillation circuit;

a controlling unit that causes spreading of a power spectrum of the oscillation signal by switching switches, at a predetermined timing, the phase comparator between a first state where the phase difference signal is output from the phase comparator and a second state where the phase difference signal is disabled and not output from the phase comparator; and

a resistor element that is disposed between a predetermined potential and a signal line for supplying the phase difference signal from the phase comparator to the low pass filter,

when the phase difference signal is output from the phase comparator, the oscillation circuit performing oscillation operation based on the voltage signal corresponding to the phase difference signal,

when the phase difference signal is disabled, the low pass filter being supplied with the predetermined potential through the resistor element to allow the oscillation circuit to perform oscillation operation based on the voltage signal generated depending on the supplied predetermined potential.

2. (Previously presented) The PLL circuit of claim 1, wherein

~~the phase comparator includes a charge pump to output the phase difference signal to is disposed between the phase comparator and the low-pass filter to convert the level of the phase difference signal~~, and wherein

when the ~~control signal~~ phase comparator is switched to the second state to disable the phase difference signal, the controlling unit performs control to set an output level of the charge pump to high impedance.

3. (Currently amended) The PLL circuit of claim 1, wherein

the resistance value of the resistor element is set depending on the extent of spreading the power spectrum ~~correlated with the oscillation frequency~~ of the oscillation signal.

4. (Currently amended) The PLL circuit of claim 1, wherein

the period for which the phase difference signal is disabled is set depending on the extent of attenuating the peak level of the power spectrum ~~correlated with the oscillation frequency~~ of the oscillation signal.

5. (Currently amended) The PLL circuit of claim 1, wherein

the period for which the phase difference signal is disabled is set depending on the extent of spreading the power spectrum ~~correlated with the oscillation frequency~~ of the oscillation signal.